

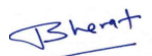
Dept. of ECE, MNIT Jaipur

Time Table

Odd Semester 2021-22

## V Semester (III Year) B.Tech. ECE

	8-9 AM	9-10 AM	10-11 AM	11-12 PM	12-1 PM	1-2 PM	2-3 PM	3-4 PM	4-5 PM	
Monday		ECT305 OCS RKM VLTC L302	ECT301 MW Engg. S.S VLTC L302	ECT302 DSP KKS VLTC L302	Lunch	MW Engg. Lab; EC1; Comm. Lab; S.S				
						DCS Lab; EC2; Adv. Elect. Lab; RPY				
						Dig. CMOS IC Lab; EC3; Dig. & $\mu$ P Lab; C.S				
						OCS Lab; EC4; Opt. Comm. Lab; RKM	DSP Lab; EC4; IMPACT Lab; KS			
Tuesday		ECT306 VLSI T&T MY VLTC L302	ECT303 DCS RPY VLTC L302	ECT302 DSP KKS VLTC L302	Lunch	MW Engg. Lab; EC2; Comm. Lab; RM				
						DCS Lab; EC1; Adv. Elect. Lab; RPY				
						Dig. CMOS IC Lab; EC4; Dig. & $\mu$ P Lab; C.S				
						OCS Lab; EC3; Opt. Comm. Lab; RKM	DSP Lab; EC3; IMPACT Lab; KKS			
Wednesday		ECT306 VLSI T&T MY VLTC L302	ECT305 OCS RKM VLTC L302	ECT303 DCS RPY VLTC L302	Lunch	ECT301 MW Engg. S.S VLTC L302	ECT304 Dig. CMOS IC C.S VLTC L302			
Thursday	ECT306 VLSI T&T MY VLTC L302	ECT304 Dig. CMOS IC C.S VLTC L302	ECT305 OCS RKM VLTC L302	ECT303 DCS RPY VLTC L302		ECT302 DSP KKS VLTC L302	ECT301 MW Engg. S.S VLTC L302			
Friday		MW Engg. Lab; EC4; Comm. Lab; RM		ECT304		Lunch	MW Engg. Lab; EC3; Comm. Lab; RM			
		DCS Lab; EC3; Adv. Elect. Lab; RPY		Dig. CMOS IC			DCS Lab; EC4; Adv. Elect. Lab; IS			
		Dig. CMOS IC Lab; EC2; Dig. & $\mu$ P Lab; C.S		C.S	DSP Lab; EC1; IMPACT Lab; K.S		CMOS IC Lab; EC1; Dig. & $\mu$ P Lab;			
		OCS Lab; EC1; Opt. Comm. Lab; RKM		VLTC L302	OCS Lab; EC2; Opt. Comm. Lab; RKM		DSP Lab; EC2; IMPACT Lab; KKS			



(Dr. Bharat Choudhary)



(Dr. Rajesh Saha)



(Prof. Vijay Janyani)