Two Day Workshop on

Security, Architecture, Synthesis and Validation for System-On- Chip

Expert: Prof. Sandip Ray, Professor, University of Florida, USA

(Under TEQIP II and E & ICT Academy, MNIT-Jaipur)

Day 1: 5/7/2019
- Lecture 1:   10.30 am to 12.30 pm
  Topic: Introduction to System-on-Chip Platform Security: Architecture, Synthesis, Validation, and Debug

- Lecture 2:   2.00 pm to 4.00 pm
  Topic: A Centralized Architecture for Implementing SoC Security Policies: Impact on Architecture and Validation

Day 2: 6/7/2019
- Lecture 3: 10.30 am to 12.30
  Topic: Case Study: Introduction to Robustness in Modern Automotive Systems: Safety and Security

- Lecture 4:   2.00 pm to 4.00 pm
   Topic: SoC Security in Automotive Robustness: Approaches and Advances

Details: The first lecture will introduce the topic of SoC Security, and current best practices. In the second lecture we will look carefully into one of the specific security architecture, referred to as E-IIPS. The goal is to study how this architecture addresses many of the SoC security challenges in the emergent world of smart and connected devices. In the third lecture we will introduce self-driving automotive applications as a specific case of connected application security. Finally, lecture 4 will give a sense of how the SoC security solutions are integrated into such applications and the repercussions of that.

Registration Fees:
Outside participants :1000 INR
MNIT Faculty/Research scholar are free.